



PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Yojiro MATSUEDA et al.

Application No.: 10/676,072

Filed: October 2, 2003

Docket No.: 117391

For: CIRCUIT SUBSTRATE, MANUFACTURING METHOD THEREOF,  
ELECTRO-OPTICAL DEVICE, AND ELECTRONIC APPARATUS

LETTER TO THE OFFICIAL DRAFTSPERSON

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Please substitute the attached 7 sheets of formal drawings depicting Figures 1-8F for the corresponding drawings filed with the application. The attached drawings correspond to the facsimile copies of the drawings filed on October 2, 2003.

Respectfully submitted,

James A. Oliff  
Registration No. 27,075

Eric D. Morehouse  
Registration No. 38,565

JAO:EDM/gam

Date: October 23, 2003

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